

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 02/18/04  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Lid and Method of Employing A Lid  
on an Integrated Circuit  
Attorney Docket Number:: X-1389-1P US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 5  
Small Entity?:: No  
Petition included?:: NO

### APPLICANT INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Paul  
Middle Name:: Ying-Fung  
Family Name:: Wu  
Street:: 20090 Edinburgh Drive  
City:: Saratoga  
State or Province:: CA  
Postal or Zip Code:: 95070

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Soon-Shin  
Family Name:: Chee  
Street:: 1602 Grosbeak Avenue  
City:: Sunnyvale  
State or Province:: CA

Postal or Zip Code:: 94087  
 Applicant Authority Type:: Inventor  
 Primary Citizenship Ctry:: US  
 Status:: Full Capacity  
 Given Name:: Steven  
 Middle Name:: H.C.  
 Family Name:: Hsieh  
 Street:: 20861 Hanford Drive  
 City:: Cupertino  
 State or Province:: CA  
 Postal or Zip Code:: 95014

#### **CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 24309

#### **REPRESENTATIVE INFORMATION**

<b>Representative Customer Number::</b>	<b>24309</b>	
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#### **DOMESTIC PRIORITY INFORMATION**

<b>Application::</b>	<b>Continuity Type::</b>	<b>Parent Application::</b>	<b>Parent Filing Date::</b>
This Applic.	Continuation In Part	10/648,118	8/25/2003

#### **ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
 Street:: 2100 Logic Drive  
 City:: San Jose  
 State or Province:: California  
 Postal or Zip Code:: 95124